

2.44 kV Ga₂O₃ vertical trench Schottky barrier diodes with very low reverse leakage current

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Abstract—High-performance β -Ga₂O₃ vertical trench Schottky barrier diodes (SBDs) are demonstrated on bulk Ga₂O₃ substrates with a halide vapor phase epitaxial layer. A breakdown voltage (BV) of 2.44 kV, Baliga's figure-of-merit (BV²/R_{on}) of 0.39 GW/cm² from DC measurements and 0.45 GW/cm² from pulsed measurements are achieved, all of which are the highest among β -Ga₂O₃-based power devices. A lowest reverse leakage current density below 1 μ A/cm² until breakdown is observed on devices with a fin width of 1-2 μ m, thanks to the reduced surface field (RESURF) effect provided by the trench SBD structure. The specific on-resistance is found to reduce with increasing area ratio of the fin-channels following a simple relationship. The reverse leakage current agrees well with simulated results considering the barrier tunneling and barrier height lowering effects. The breakdown of the devices is identified to happen at the trench bottom corner, where a maximum electric field over 5 MV/cm could be sustained. This work marks a significant step toward reaching the promise of a high figure-of-merit in β -Ga₂O₃.

I. INTRODUCTION

β -Ga₂O₃ has seen increasing research effort in recent years towards the realization of high-performance electronic devices. A Baliga's figure of merit (FOM) of β -Ga₂O₃ projected to well exceed that of GaN and 4H-SiC arises from a combination of an ultra-wide bandgap of \sim 4.5 eV, therefore a high expected critical electric field of up to 8 MV/cm [1] and a decent room temperature electron mobility of \sim 200 cm²/V·s [2, 3]. These properties make β -Ga₂O₃ an excellent material candidate for next-generation power electronic devices, especially under harsh environment. The availability of melt-growth methods for single-crystal bulk substrate provides added advantage towards lower cost and a head start for fast development of epitaxial growth and device technologies. With the availability of high quality halide vapor phase epitaxial (HVPE) layers with a net doping concentration around \sim 2 \times 10¹⁶ cm⁻³, β -Ga₂O₃ power electronic devices has well-surpassed the unipolar material limit of Si, exemplified by the demonstration of kilovolt-class enhancement-mode transistors [4] and Schottky barrier diodes (SBDs) [5, 6]. However, the FOM of the present devices are still far from the projected material limit. In this

work, we demonstrate record high-performance vertical β -Ga₂O₃ SBDs through (i) effectively reduced leakage current with the reduced surface field (RESURF) effect offered by a trench MIS-type SBD structure [7-9] and (ii) smoothly-etched trench profile. A record high breakdown voltage (BV) of 2444 V, and a record high FOM (BV²/R_{on}) are achieved without other dedicated field management techniques. The diodes show near ideal behavior in both forward and reverse operations well predicted by physics-based models.

II. DEVICE DESIGN AND FABRICATION

Fig. 1 shows the schematic cross-section of the Ga₂O₃ trench SBDs. The drift layer consists a 10- μ m HVPE n-Ga₂O₃ grown on a (001) n-type Ga₂O₃ substrate. Majority of the fin widths (W_{fin}) are 1-4 μ m and the trench depth is 1.55 μ m. The fin area ratio is defined to be the fin width over the pitch size.

Fig. 2 illustrates the fabrication process of the trench SBDs. First, the trench was dry etched using a BCl₃/Ar gas mixture and Ti/Pt as the hard mask [10]. After the dry etch and mask removal, wet acid treatment was performed to remove the dry etch induced damage. A rounded trench corner profile is realized as a result of the dry and wet etching as shown in the scanning electron microscope (SEM) cross-section image (**Fig. 4**), which is desired to reduce field crowding. The back ohmic contact was formed by a Ti/Au deposition and a rapid thermal anneal for 1 min in N₂. Then, a 100-nm Al₂O₃ dielectric was deposited by atomic layer deposition (ALD) and opened by dry etching for the Schottky contact on top of the fins. Finally, Ni Schottky contact was deposited by e-beam evaporation, followed by Ti/Pt sputtering for the sidewall metal coverage. **Fig. 3** shows an optical graph of the top view of a fabricated device with a fin length of 150 μ m and W_{fin} of 2 μ m. *The entire central anode area within the dashed lines is used for the calculation of current density throughout the discussion.*

III. RESULTS AND DISCUSSION

Capacitance-voltage measurements were performed on the regular SBDs co-fabricated on the same sample. **Fig. 5** plots the extracted net doping concentration profile from the measurements on both the SBD made on the original epitaxial surface as well as the SBD made on the etched planar surface formed by the trench-etch step. The doping concentration

increases from $\sim 1 \times 10^{16} \text{ cm}^{-3}$ near the epi-surface to $\sim 2 \times 10^{16} \text{ cm}^{-3}$ beyond a depth of $\sim 2.5 \text{ }\mu\text{m}$. The $1/C^2$ plot is shown in **Fig. 6**. A built-in potential (V_{bi}) of $1.25 \pm 0.1 \text{ V}$ is extracted, corresponding to a Schottky barrier height ($q\phi_B$) of $1.4 \pm 0.1 \text{ eV}$.

Fig. 7 shows the forward I-V characteristics of the trench SBDs in comparison with the regular SBD measured with DC scans. The 2-4 μm trench SBDs shown in the figure all have a fin area ratio of 50%, while the 1- μm device has an area ratio of 33%. The trench SBDs and the regular SBD have a similar turn-on voltage of 1.25 V. From a fitting to the thermionic emission model, $q\phi_B$ is extracted to be 1.35 eV, which agrees with the extraction from C-V measurements. The differential R_{on} of the 2-4 μm fin devices are similar, being around $11.3 \text{ m}\Omega\cdot\text{cm}^2$ and higher than that of the regular SBD ($7 \text{ m}\Omega\cdot\text{cm}^2$).

Fig. 8 shows the pulsed I-V measurements of the forward characteristics. In comparison with the DC measurements. The slightly lower turn-on voltage measured under the pulsed condition is believed to be related to the trapping effect that is more severe during the DC scan, while the higher current at $>3 \text{ V}$ is attributed to the mitigation of device self-heating under the pulsed measurement condition. Thus, we believe the pulsed I-Vs provide more accurate measurements of the R_{on} determined by the intrinsic conduction properties of the drift layer.

As shown in **Fig. 9**, the R_{on} values extracted using the pulsed measurements are compared among all types of SBDs. It is found that the SBD made on the etched surface with an 8.55- μm drift layer has the lowest R_{on} of $4 \text{ m}\Omega\cdot\text{cm}^2$, while the SBD with a 10- μm drift layer has an R_{on} of $7 \text{ m}\Omega\cdot\text{cm}^2$. The difference is due to the top 1.55 μm drift layer, which has a net doping concentration of $\sim 1 \times 10^{16} \text{ cm}^{-3}$. The trench SBDs have a R_{on} of $10 \text{ m}\Omega\cdot\text{cm}^2$, which matches well with the expected value considering the 50% area ratio leads to a doubled contribution from the 1.55 μm top layer to the specific R_{on} . A simple model for the specific R_{on} of the trench SBDs can be thus developed:

$$R_{on,sp} = 4 + \frac{3}{\text{area ratio}} \text{ m}\Omega \cdot \text{cm}^2 \quad (1)$$

Fig. 10 shows the statistics of the extracted R_{on} for the trench SBDs with different W_{fin} and area ratio. The R_{on} of the 2-8 μm devices follows the trend expressed by (1) very well. For the 1- μm devices, (1) has to be modified reflecting a reduced effective fin width for best fitting, which indicates that there may be some etching damage and/or trapping effect at the fin sidewall, which is most dominant for the 1- μm fin devices.

Fig. 11 plots the simulated electric field profile along vertical cut-lines at the fin center (dash line in **Fig. 1**). The field near the surface is effectively reduced by the trench structure. The RESURF effect is more prominent for smaller fin width.

Fig. 12 shows the representative reverse I-V characteristics of the trench SBDs in comparison with the regular SBDs. The reverse leakage current of the trench SBDs is much lower than the regular SBDs, and the BV is much higher, reaching a record value of 2.44 kV in the 1- μm devices. The reverse leakage current is lower than 1 mA/cm^2 before breakdown for the 1-3

μm devices, a typical value used to specify the reverse blocking voltage for commercial SBDs. Even lower leakage current beyond the detection limit is observed in some of the devices, as shown in **Fig. 13**. The leakage current of the lower leakage devices follows the simulated reverse I-V characteristics considering the barrier tunneling and barrier-height lowering effect. An electron effective mass of $0.3 m_0$ [3] and the extracted barrier height of 1.4 eV is used in the simulation.

The statistics of the BV is shown in **Fig. 14**. The BV is found to increase with decreasing fin width. No correlation between the BV and the area ratio is observed. To identify the breakdown mechanism, the electric field profile is simulated. **Fig. 15** and **Fig. 16** shows the simulated electric field profile along a horizontal outline across the trench bottom corner, at a fixed voltage of 2 kV and around the highest breakdown voltage, respectively. It is observed that the field crowding happens around the trench corner and the field peak increases with the fin width at a fixed voltage. As shown in **Fig. 16**, the field peaks near a similar value of $\sim 5.9 \text{ MV/cm}$ at the highest BV for each fin width, indicating that the breakdown happens at the trench corner. Note that the 5.9 MV/cm value is simulated considering a trench corner angle of 90° . In reality the value should be lower due to the rounded trench corner profile.

IV. CONCLUSION

Exploiting pronounced RESURF effects in the trench SBD structure and the smoothly etched trench corner profile, the reverse leakage current is effectively suppressed in the Ga_2O_3 trench SBDs and high breakdown voltages are achieved. **Fig. 17** benchmarks the state-of-the-art $\beta\text{-Ga}_2\text{O}_3$ SBDs [5, 6, 8, 11-13]. Our 1- μm device achieved a record high BV and our best 2- μm device with a 50% area ratio achieved a record FOM of 0.39 GW/cm^2 (2096 V, $11.3 \text{ m}\Omega\cdot\text{cm}^2$) from DC scan and 0.45 GW/cm^2 (2096 V, $9.8 \text{ m}\Omega\cdot\text{cm}^2$) from pulsed measurements. The R_{on} can be further reduced by increasing the fin area ratio. The improved BV and FOM from the current state-of-the-art further unveils the excellent material properties of $\beta\text{-Ga}_2\text{O}_3$, hence an attractive platform for power electronic devices.

ACKNOWLEDGMENT

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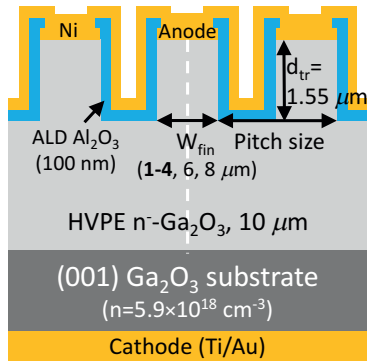


Fig. 1. Schematic cross-section of the Ga₂O₃ trench Schottky barrier diodes. Fin widths (W_{fin}) of 1-4, 6, 8 μm are designed with different fin area ratio ($W_{fin}/pitch$ size). The trench depth (d_{tr}) is 1.55 μm.

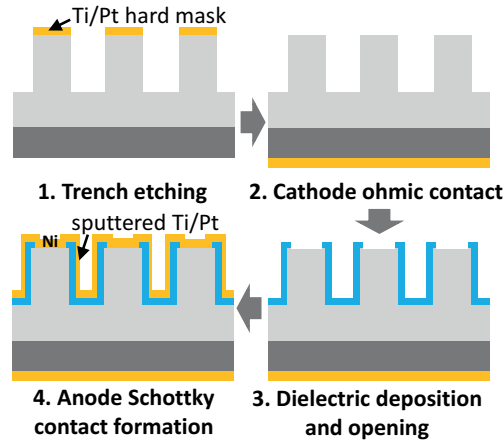


Fig. 2. Fabrication process flow of the trench SBDs.



Fig. 3. Optical top view image of a fabricated device with a W_{fin} of 2 μm and a fin area ratio of 50%. The length of the fins is 150 μm. *Central anode area within the dashed lines is used for current density calculation.*

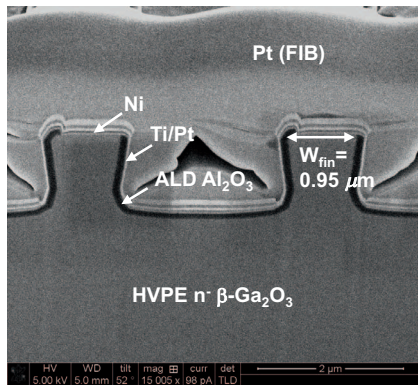


Fig. 4. Scanning electron microscopy (SEM) cross-section image of a device with a designed fin width of 1 μm. A slightly inward-slanted sidewall profile and rounded trench corners are observed, preferable for improved RESURF effect and reduced field crowding.

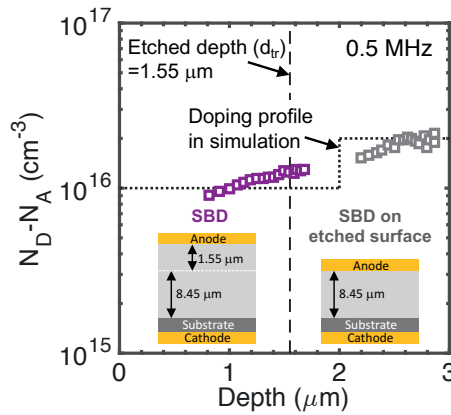


Fig. 5. Extracted net doping concentration ($N_D - N_A$) from C-V measurements. Two types of regular Schottky barrier diode are used for the measurements with the cross-sections shown in the insets. Dotted line shows the approximated doping profile used in the TCAD simulation.

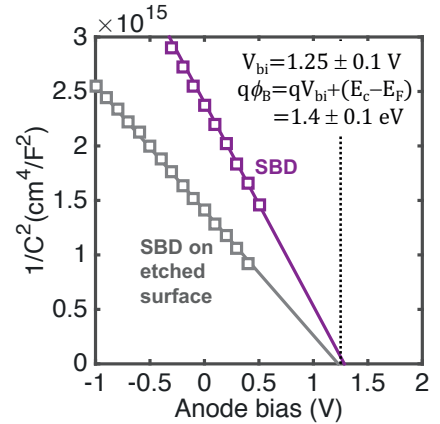


Fig. 6. $1/C^2$ plot for the SBD and the SBD on the etched surface. The V_{bi} is extracted to be ~ 1.25 V, corresponding to a barrier height ($q\phi_B$) of ~ 1.4 eV.

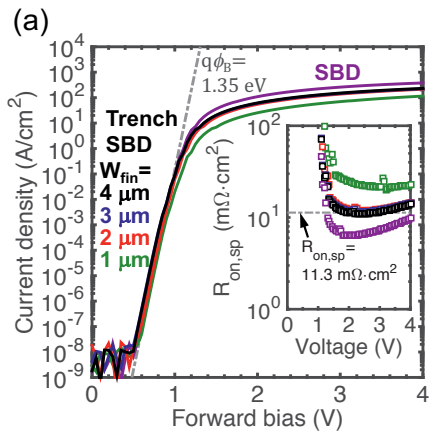


Fig. 7. Forward I-V characteristics (a) in log scale and (b) in linear scale of the trench SBDs in comparison with the regular SBD, measured by DC scans. *The current density of the trench SBDs is normalized by the central anode area (see Fig. 3).* A barrier height ($q\phi_B$) of 1.35 eV is extracted from the thermionic emission model using a reduced effective Richardson constant A^{**} of 33.1 A/cm²·K² [5], which agrees with the extracted barrier height by C-V.

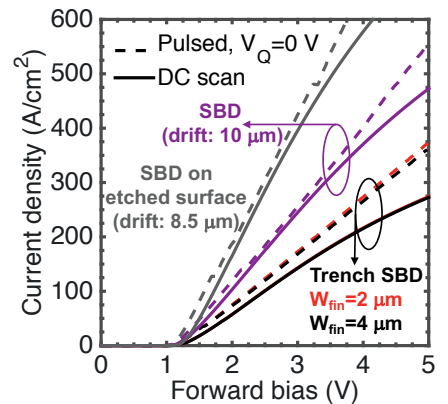
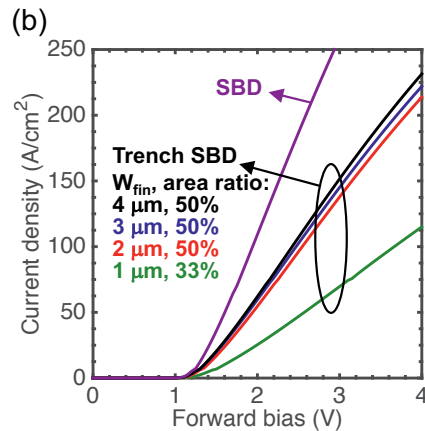


Fig. 8. Comparison of the forward I-V characteristics under pulsed condition versus DC. A pulse width of 8.4 μs and a duty cycle of 0.84% is used. The observed difference is likely due to a combination of device self-heating and trapping effect.

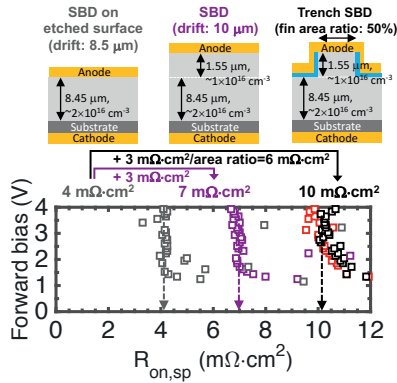


Fig. 9. Extracted specific differential on-resistance ($R_{on,sp}$) of the devices from pulsed I-V measurements. $R_{on,sp}$ of the trench SBDs is well-explained by considering the 50% fin area ratio.

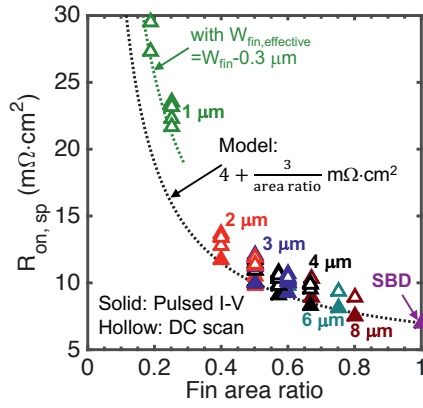


Fig. 10. Statistics of the extracted specific R_{on} of the trench SBDs. The trend of R_{on} agrees well with a simple model considering the area ratio for the devices with 2-8 μm fin widths, while a modified model with a reduced effective W_{fin} is found to match the 1- μm device data.

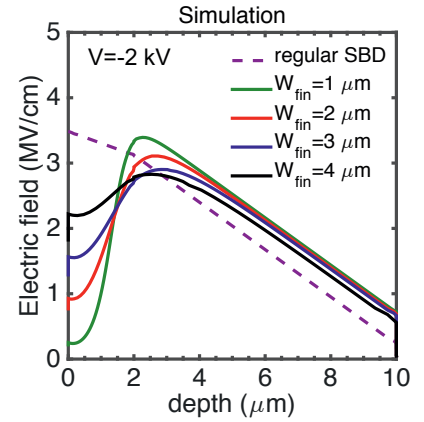


Fig. 11. Simulated electric field profile along vertical cut-lines at the fin center (see the dash line in Fig. 1) at a reverse bias of 2 kV. The surface field is reduced effectively with the trench-MIS structure.

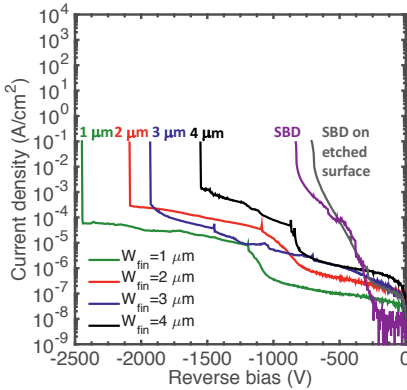


Fig. 12. Representative reverse I-V characteristics of the trench SBDs in comparison with the regular SBDs. Much lower leakage current below $1 \text{ mA}/\text{cm}^2$ is observed for the 1-3 μm devices in comparison with the regular SBDs, together with much higher breakdown voltages.

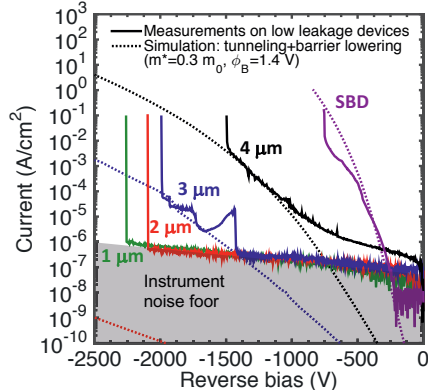


Fig. 13. Representative I-V characteristics of the trench SBDs with low leakage current. The leakage current profile agrees well with the simulation considering barrier tunneling and barrier height lowering. Effective mass of $0.3 m_0$ and the extracted $q\phi_B$ of 1.4 eV is used in the TCAD Sentaurus simulation.

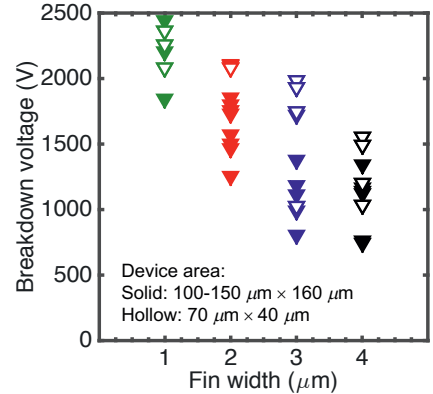


Fig. 14. Statistics of the measured breakdown voltage of the trench SBDs. The BV is found to increase with the reduction of the fin width.

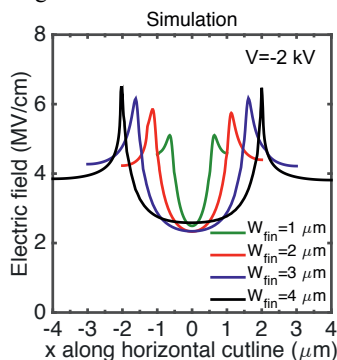


Fig. 15. Simulated electric field profile along a horizontal cutline across the trench bottom corner at 2 kV. The electric field at the trench corner is found to increase with increasing fin width.

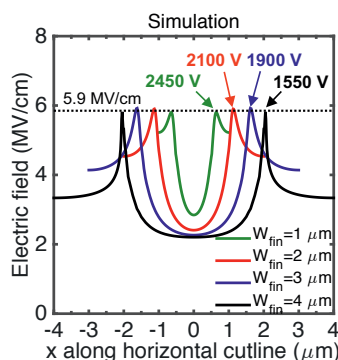


Fig. 16. Simulated electric field profile along a horizontal cutline across the trench bottom around the highest BV of each fin width. The field peak is about the same for all fin widths, indicating the device breakdown is limited by the breakdown at the trench corner.

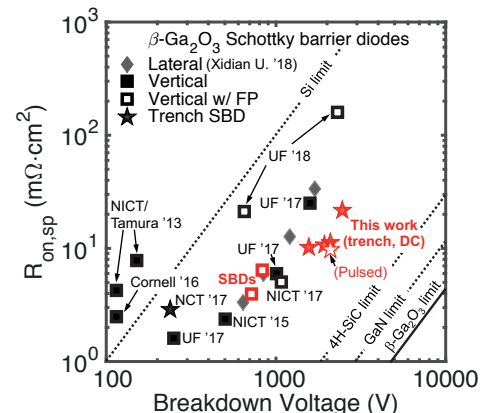


Fig. 17. Benchmark plot of $\beta\text{-Ga}_2\text{O}_3$ Schottky barrier diodes [5, 6, 8, 11-13]. Our 1- μm trench SBD achieves the highest BV, while the 2- μm trench SBD achieves the highest FOM, even without dedicated field management techniques.