Export Citatio

Near-ideal reverse leakage current and practical maximum electric field in β -Ga₂O₃ Schottky barrier diodes **5**

Cite as: Appl. Phys. Lett. **116**, 192101 (2020); doi: 10.1063/5.0007715 Submitted: 16 March 2020 · Accepted: 25 April 2020 · Published Online: 11 May 2020

Wenshen Li,^{1,a)} (Devansh Saraswat,² Yaoyao Long,² Kazuki Nomoto,¹ Debdeep Jena,^{1,2,3} (D and Huili Grace Xing^{1,2,3,a)} (D

AFFILIATIONS

¹School of Electrical and Computer Engineering, Cornell University, Ithaca, New York 14853, USA
 ²Department of Materials Science and Engineering, Cornell University, Ithaca, New York 14853, USA
 ³Kavli Institute at Cornell for Nanoscale Science, Cornell University, Ithaca, New York 14853, USA

Note: This paper is part of the Special Topic on Ultrawide Bandgap Semiconductors. ^{a)}Authors to whom correspondence should be addressed: wI552@cornell.edu and grace.xing@cornell.edu

ABSTRACT

We investigate the intrinsic reverse leakage mechanisms in Ni-based Schottky barrier diodes (SBDs) fabricated on a $(\overline{2}01)$ single crystal β -Ga₂O₃ substrate, where a uniform bulk reverse leakage current has been designed and confirmed. The temperature-dependent reverse leakage characteristics are analyzed by a numerical reverse leakage model, which includes both the image-force lowering and doping effects. We found that the reverse leakage current is near-ideal and dominated by Schottky barrier tunneling throughout the entire range of the surface electric field from 0.8 MV/cm to 3.4 MV/cm. The extracted barrier height from the reverse leakage model is consistent with the values extracted from the forward current–voltage and capacitance–voltage measurements. The practical maximum electric field, defined by the maximum allowable reverse leakage current levels, is calculated as a function of the barrier height. These results suggest that it is possible to approach the intrinsic breakdown electric field in β -Ga₂O₃ SBDs, as long as a sufficiently high barrier height (~2.2 to 3 eV) is employed.

Published under license by AIP Publishing. https://doi.org/10.1063/5.0007715

 β -Ga₂O₃ has been under intensive research as a promising ultrawide-bandgap semiconductor material.¹ It is expected to have a high breakdown electric field of up to 8 MV/cm due to the sizable bandgap of 4.5–4.7 eV,^{2.3} as well as a decent electron mobility of up to ~200 cm²/V s.^{4.5} These properties yield a Baliga figure-of-merit (BFOM) higher than GaN and 4H-SiC, thus making Ga₂O₃ a strong material candidate for high power devices.⁶ In addition, melt-growth techniques for Ga₂O₃ substrates are available,⁷ which promises a cost-effective device platform.

The past few years have witnessed fast-paced advancements in Ga_2O_3 power devices. Over 2-kV Schottky barrier diodes (SBDs),⁸⁻¹⁰ as well as high-voltage power transistors¹¹⁻¹⁴ have been demonstrated. A high BFOM of 0.95 GW/cm² has been achieved in trench SBDs,¹⁵ which is already comparable with that in state-of-the-art GaN unipolar devices.

Due to the difficulty in native p-type doping,¹⁶ Schottky barrier diodes may be the most important rectifiers in Ga_2O_3 . Although promising performance in Ga_2O_3 SBDs has been demonstrated, there have not been many studies on the intrinsic reverse leakage mechanisms in Ga_2O_3 SBDs. This subject is of high importance since the

breakdown voltage or maximum surface electric field ($E_{\rm max}$) of SBDs based on wide-bandgap semiconductors is typically limited by the leakage current, rather than the material's breakdown electric field. Understanding the intrinsic leakage current mechanisms will allow accurate evaluation of $E_{\rm max}$ practically allowable in SBDs, which we refer to as the *practical maximum electric field*.

Previous studies on the leakage mechanisms have been carried out mostly under relatively low surface electric fields (*E*) below 2 MV/cm, where thermionic emission (TE)^{17,18} or thermionic-field emission (TFE)^{19–21} dominates. To observe and identify the intrinsic reverse leakage current, especially under a higher surface electric field, several challenges need to be addressed. First, edge leakage current due to electric-field crowding should be suppressed and uniform bulk leakage current should be confirmed. We have shown that the edge leakage in small devices and/or with very low doping concentrations in the drift layer can be prominent even with E < 1 MV/cm.²⁰ Second, the material and Schottky interface need to be of high-enough quality such that the defect-related leakage process is insignificant. Third, the image-force lowering (IFL) effect should be properly considered in

the analysis of the leakage characteristics, which is especially critical for the TE and TFE currents. Previous reports have claimed Poole–Frenkel,²² trap-assisted-tunneling,^{23,24} and variable-range hopping processes^{21–24} in Ga₂O₃ SBDs, which we believe most likely resulted from one or combinations of these three challenges in study-ing SBDs, but not necessarily the material quality of Ga₂O₃ alone.

In this work, by addressing the three challenges outlined above, we are able to observe and identify near-ideal reverse leakage current of Ga₂O₃ SBDs up to a surface electric field of 3.4 MV/cm. In addition, the practical maximum electric field in β -Ga₂O₃ SBDs is calculated as a function of the barrier height.

Under a high surface electric field in SBDs, tunneling processes— TFE and FE (field emission)—dominate the reverse leakage current. TFE and FE processes have been analytically examined by Murphy and Good,²⁵ and later by Padovani and Stratton.²⁶ However, Murphy and Good's models assume a triangular barrier, and thus, the doping effect in semiconductors is not considered.²⁵ Padovani and Stratton's models consider the doping effect; however, IFL is neglected.²⁶ To properly include both the IFL and doping effects, we developed a numerical reverse leakage model for the analysis of the reverse leakage characteristics.

The total reverse leakage current density (J) is given by 2^{25}

$$J = \frac{A^*T}{k_{\rm B}} \int_{\mathcal{E}_{\rm min}}^{+\infty} \mathcal{T}(\mathcal{E}) \times \ln \left[1 + \exp\left(-\frac{\mathcal{E} - \mathcal{E}_{\rm Fm}}{k_{\rm B}T}\right) \right] d\mathcal{E}, \qquad (1)$$

where $A^* = 4\pi m^* k_B^2 e/h^3$ is the Richardson constant, \mathcal{E} is the electron energy, \mathcal{E}_{Fm} is the Fermi-level energy in metal, and \mathcal{E}_{min} is the minimum energy for the tunneling process to occur, which is equal to the effective constant potential energy inside the metal.²⁵ $\mathcal{T}(\mathcal{E})$ is the transmission probability across the barrier. If \mathcal{E}_{Fm} is taken as the zeroenergy level, the potential energy distribution of the Schottky barrier under IFL is given by

$$\mathcal{E}_{\rm c}(x) = e\phi_{\rm B} - eEx - \frac{e^2}{16\pi\varepsilon_{\rm s}x} + \frac{e^2N_{\rm D}x^2}{2\varepsilon_{\rm s}}, \qquad (2)$$

where $\phi_{\rm B}$ is the Schottky barrier height, *E* is the surface electric field, $N_{\rm D}$ is the net donor concentration, and $\varepsilon_{\rm s}$ is the dielectric constant of β -Ga₂O₃ (10 ε_0).²⁷ The 3rd and 4th terms in Eq. (2) capture the IFL effect and the doping effect, respectively. Due to IFL, the top of the barrier is rounded and lowered by $\Delta \phi = \sqrt{eE/(4\pi\varepsilon_{\rm s})}$, such that $\mathcal{E}_{c,\text{max}} = e(\phi_{\rm B} - \Delta \phi)$, as illustrated in Fig. 1(a). It is important to consider this barrier-rounding effect in detail for an accurate evaluation of the tunneling probability, by using Eq. (2). It may be tempting to simply replace $\phi_{\rm B}$ with $\phi_{\rm B} - \Delta \phi$ to reflect IFL, as shown in Fig. 1(b). However, this would significantly overestimate the tunneling probability and, thus, an improper way of treating IFL. Under a Wentzel-Kramers-Brillouin (WKB)-type approximation, $\mathcal{T}(\mathcal{E})$ is given by²⁵

$$\mathcal{T}(\mathcal{E}) = \begin{cases} \left[1 + \exp\left(-\frac{2i}{\hbar} \int_{x_1}^{x_2} p(x) dx\right) \right]^{-1}, & \text{if } \mathcal{E} \le \mathcal{E}_{c,\max} \\ 1, & \text{if } E > \mathcal{E}_{c,\max}, \end{cases}$$
(3)

where $p(x) = -i\sqrt{2m^*(\mathcal{E}_c(x) - \mathcal{E})}$ and x_1 and x_2 are classical turning points, where $\mathcal{E}_c(x) = \mathcal{E}$. Here, $\mathcal{E}_{c,max}$ naturally separates the two reverse leakage current components: tunneling (when $\mathcal{E} \leq \mathcal{E}_{c,max}$) and thermionic emission with IFL (when $\mathcal{E} > \mathcal{E}_{c,max}$). The integrand in Eq. (1) starts to decrease exponentially as the electron energy \mathcal{E} decreases beyond a few $k_B T$ below \mathcal{E}_{Fm} , and thus, \mathcal{E}_{min} can be safely replaced by $-\infty$. Since the effective mass of conduction band electrons



FIG. 1. Schematic illustration of the barrier potential distribution under reverse bias (a) with image-force lowering (IFL) properly considered and (b) with IFL improperly considered. (c) Calculated reverse leakage current as a function of the surface electric field in β -Ga₂O₃ Schottky barrier diodes (SBDs) using our numerical model, with image-force lowering properly considered but without the doping effect. The model agrees well with Murphy and Good's thermionic field emission (TFE) and field emission (FE) models²⁵ within their applicable ranges.

in β -Ga₂O₃ is almost isotropic and there is no valley degeneracy,²⁸ we use a single effective mass for both the Richardson constant and the tunneling effective mass, with a value of 0.31 m,.²⁹

Figure 1(c) shows the calculated reverse leakage current using our numerical model with $\phi_{\rm B} = 1.4$ V. To compare with Murphy and Good's analytical models, the doping concentration is temporarily taken as zero to mimic the same condition. It can be seen that our model matches very well with Murphy and Good's TFE and FE models within their respective applicable ranges. This validates our numerical methods. In the subsequent data analysis, the doping effect will be explicitly considered in our numerical model.

Ni-Ga₂O₃ SBDs were fabricated on ($\overline{2}01$) Sn-doped β -Ga₂O₃ bulk substrates, as schematically shown in Fig. 2(a). The wafer was cleaned with acetone and methanol first and then soaked in HF and HCl for 5 min each to remove surface defects that might have resulted from wafer polishing or storage. The fabrication process begins with a deposition of Ti/Au (75/150 nm) for the cathode Ohmic contact on the backside of the wafer, followed by a rapid thermal annealing at 470 °C for 1 min under N₂ ambience to facilitate the Ohmic contact. Subsequently, the anode area is defined by photolithography followed by surface treatments consisting of a 5-min ozone treatment and acid cleaning using 30:1 buffered oxide etchant (BOE) and 1:1 diluted HCl each for 1 min to remove photoresist residues. Ni/Pt (50/30 nm) anode Schottky contacts were deposited by electron-beam evaporation and patterned by lift-off. Ga₂O₃ is prone to plasma-induced surface damage,³⁰ and thus, the Schottky contact surface was not exposed to any plasma process throughout the fabrication process. Finally, a self-aligned dry etching $(0.5 \,\mu\text{m})$ was performed for mesa isolation using a BCl₃/Ar gas mixture,³¹ with the anode metal as the hard mask. The etched sidewall is nearly vertical.³² This serves as an effective edge termination based on similar principles as shown in vertical GaN p-n diodes.³

Temperature-dependent capacitance–voltage (*C*–*V*) and forward and reverse current–voltage (*I*–*V*) measurements were performed on the fabricated SBDs. Figure 2(b) shows the extracted net doping concentration (N_D – N_A) from *C*–*V* measurements, which show a nearly constant value of ~1.45 × 10¹⁸ cm⁻³. The inset shows the 1/*C*²–*V*



FIG. 2. (a) Schematic of Ni-Ga₂O₃ SBDs fabricated on a Sn-doped ($\overline{2}$ 01) β -Ga₂O₃ bulk substrate. A self-aligned mesa isolation with an etch depth of 0.5 μ m is employed for edge termination. (b) Extracted net doping concentration from *C*-*V* measurements, showing a near constant value of 1.45×10^{18} cm⁻³. The inset shows the $1/C^2$ -*V* plot, which is used to extract the barrier height from the extrapolated built-in potential.

plot, which is used to extract the barrier height from the extrapolated built-in potential at zero bias ($V_{\rm bi,0}$) (see, for example, the method described in Ref. 19). $V_{\rm bi,0}$ values are found to be 1.30 V at 23 °C and 1.23 V at 200 °C.

Figure 3(a) shows the temperature-dependent forward *I*–*V* characteristics. The thermionic emission (TE) model is used to extract the apparent barrier height ($\phi_{B,ap}$) as well as the ideality factor (*n*) by fitting the experimental data to

$$J_{\rm TE} = A^* T^2 \exp\left(-\frac{e\phi_{\rm B,ap}}{k_{\rm B}T}\right) \left[\exp\left(\frac{eV_{\rm F}}{nk_{\rm B}T}\right) - 1\right]$$
$$= J_0 \left[\exp\left(\frac{eV_{\rm F}}{nk_{\rm B}T}\right) - 1\right], \tag{4}$$

where $V_{\rm F}$ is the forward bias, A^* has a value of 37 A cm⁻² K⁻² using m^{*} = 0.31 m₀, and J_0 is the saturation current density; a J_0 value of 1 mA/ cm² corresponds to a $\phi_{\rm B,ap}$ value of ~0.6 V at room temperature (RT). The extracted barrier heights and ideality factors are shown in Fig. 3(b). The ideality factor is 1.14 at 23 °C and decreases to 1.05 at 200 °C. Since the surface electric field at zero bias $(E_0 = \sqrt{2(N_{\rm D} - N_{\rm A})(eV_{\rm bi,0} - k_{\rm B}T)/\varepsilon_{\rm s}}$ is as high as 0.8 MV/cm due to the high doping concentration, IFL cannot be neglected. Using the



FIG. 3. (a) Temperature dependence of the forward *I–V* characteristics. Thermionic emission (TE) model is used for the data analysis. (b) Extracted barrier height and ideality factor from the TE model.

standard method,³⁴ the image-force correction ($\Delta \phi_{if}$) to the apparent height is calculated to be 0.11 V, and the image-force controlled limit for the ideality factor (n_{if}) is 1.022. After image-force (IF) correction, the barrier height is around 1.35 eV, as shown in Fig. 3(b). The slight temperature dependence of the extracted barrier height could be due to a finite but nearly negligible presence of barrier inhomogeneity^{23,35} or interface trapping.²⁰

The reverse I-V characteristics were first tested at room temperature on diodes with different diameters. Figure 4 shows the typical measured I-V curves. We have confirmed that the reverse current density has no dependence on the anode diameter, as shown in the inset of Fig. 4. This serves as a solid evidence that the edge leakage is sufficiently suppressed and that the bulk leakage current dominates. The reverse I-V characteristics can be very well-fitted using our numerical model, with the barrier height being the only fitting parameter. The barrier height value of 1.37 eV is very close to the IF-corrected barrier height extracted from forward I-V measurements. Also shown in Fig. 4 is the calculation using Murphy and Good's field emission model with IFL considered.²⁵ Due to the absence of the doping effect in Murphy and Good's FE model, the current density is overestimated by roughly a factor of 2; but the voltage/electric-field dependence is captured fairly well, indicating that the FE process is the dominant reverse leakage process at RT in our SBDs. Clearly, the numerical reverse leakage model offers better accuracy in this high-doping case, but the difference is not significant. Hence, Murphy and Good's analytical model, where the doping effect is neglected, is still a very good one to calculate the ideal leakage current in power devices since the doping level is typically lower than what is used in this work.

Figure 5 shows the temperature-dependent reverse leakage current density as a function of the surface electric field (J-E characteristics). Throughout the range of surface electric fields (0.8 MV/ cm-3.4 MV/cm) and at each temperature, the reverse J-E characteristics can be well-fitted by our numerical model, again with the barrier height as the only fitting parameter. This suggests that the measured reverse leakage current is near-ideal. Furthermore, we have plotted the thermionic emission components alone within the total reverse leakage currents. It can be seen that the contribution of thermionic



FIG. 4. Representative measured reverse I-V characteristics at RT and the calculations using our numerical model and Murphy and Good's FE model. Inset: reverse I-V characteristics on devices with different diameters (three devices per group) at RT, showing no size dependence.

ARTICLE



FIG. 5. Temperature-dependent reverse leakage current density as a function of the surface electric field (*J*–*E* characteristics). The numerical reverse leakage model is used for data fitting, with the barrier height being the only fitting parameter. The contribution from thermionic emission (TE) alone in the presence of image-force lowering (IFL) is also shown.

emission throughout the electric field range is insignificant, and thus, the main leakage mechanism in these SBDs is barrier tunneling.

To further verify the barrier tunneling process: TFE or FE, the Fowler–Nordheim (F–N) plot, or $J/E^2-1/E$ plot was generated from the temperature-dependent reverse J-E characteristics, as shown in Fig. 6. The curve at RT shows good linearity in the F–N plot, characteristic of the FE process. On the other hand, the curves start to deviate from linearity as temperature increases, especially when the surface electric field is low, suggesting the presence of TFE. The transition from FE to TFE with the decrease in the surface electric field is as expected, as shown previously in Fig. 1(c).

The barrier height values extracted from the reverse I-V characteristics are in excellent agreement with the values extracted from the forward I-V characteristics [Fig. 7(a)] and C-V measurements [Fig. 7(b)], with a maximum difference of only 0.06 eV. This further supports the validity of our numerical reverse leakage modeling that considers of both IFL and doping effects properly.

Using the numerical reverse leakage model, we calculate the maximum practical surface electric field as a function of the barrier height in β -Ga₂O₃ SBDs, defined at a maximum reverse leakage current density (J_{max}) of 1 mA/cm² or 100 mA/cm², as shown in Fig. 8. The value of 1 mA/cm² is the typical reverse leakage current density at the rated blocking voltage in commercial SBDs,³⁶ and 100 mA/cm² serves as a more relaxed criterion. To allow for generalization of the results, the doping effect is neglected in the calculation. This can be justified since the absence of the doping effect will not induce significant errors. Specifically, we have verified that even with a net doping concentration of 2×10^{18} cm⁻³, the maximum error induced by neglecting the doping effect is smaller than 0.09 MV/cm. Excellent agreement is observed between our calculation and experimental data, both from this work and from the literature.^{23,24,37} The near-ideal reverse leakage characteristics in those previously reported SBDs were not recognized, likely because a comprehensive analysis was not performed.

It can be seen that the practical E_{max} increases almost linearly with the barrier height. This suggests that it is possible to reach an E_{max} value close to the intrinsic breakdown electric field of β -Ga₂O₃ (6–8 MV/cm) as long as a sufficiently high barrier height (>2.2 eV) is implemented. In



FIG. 6. Fowler–Nordheim (F–N) plot of the temperature-dependent reverse *J–E* characteristics, as well as the fitting using the numerical reverse leakage model.

fact, Schottky barrier heights of 2.2–2.4 eV have been realized with oxidized metal contacts on β -Ga₂O₃,³⁵ and thus, this possibility may become a reality in the future. A further increase in the barrier height beyond half of the bandgap energy may be very challenging.³⁵ However, interfacial dipole engineering could be exploited as a potential way to increase the barrier height even further, as demonstrated in Ref. 38.

It is worth noting that below a threshold barrier height value, the calculated practical E_{max} drops to zero. This means that below this threshold barrier height, the diode saturation current J_0 exceeds the pre-defined maximum leakage currents J_{max} . According to the ideal diode I-V equation without barrier tunneling, which is essentially Eq. (4) assuming n = 1, an ideal diode will allow a current flow at J_0 when a reverse bias is higher than a few times the thermal voltage $k_{\text{B}}T/e$. SBDs with a ϕ_{B} value lower than the threshold value can still be made of course, but they suffer from leakage currents higher than the J_{max} desired by power electronics.

In conclusion, with the edge leakage suppressed and plasma damage to the Schottky contact interface avoided, near-ideal reverse leakage current is observed in β -Ga₂O₃ SBDs up to a surface electric field of 3.4 MV/cm. The reverse leakage characteristics can be well-fitted with our numerical model, which considers both the image-force lowering and the doping effects properly. Throughout the whole electric field



FIG. 7. Extracted barrier heights from (a) forward and reverse *I*–*V* characteristics; (b) *C*–*V* measurements. Good agreement between different extracted methods is observed with a maximum difference of \sim 0.06 eV.

scitation.org/journal/apl



FIG. 8. Calculated practical maximum electric field (E_{max}) in β-Ga₂O₃ SBDs defined at fixed maximum reverse leakage current density levels ($J_{max} = 1 \text{ mA/cm}^2$, 100 mA/cm²), as a function of the barrier height. The doping effect is neglected in the calculations, but the results are applicable to a wide range of doping concentrations, with a maximum error of 0.09 MV/cm when N_D - $N_A \le 2 \times 10^{18} \text{ cm}^{-3}$. Experimental data from this work and from the literature^{23,24,37} are also shown (hollow for $J_{max} = 100 \text{ mA/cm}^2$ and solid for $J_{max} = 1 \text{ mA/cm}^2$).

range of 0.8 MV/cm–3.4 MV/cm, near-ideal barrier tunneling dominates the leakage current instead of thermionic emission or trapassisted tunneling. The practical maximum electric field, defined by the maximum allowable reverse leakage current levels, is calculated as a function of the barrier height. The results suggest the possibility of approaching the intrinsic breakdown electric field in Ga₂O₃ SBDs with a sufficiently high barrier height of ~2.2 to 3 eV but no need to utilize the entire bandgap of the semiconductor. This implies that there is no need to develop p–n homojunctions in Ga₂O₃, and a high-quality p–n heterojunction with a sufficiently large built-in potential will serve well in designing Ga₂O₃ power electronic devices. This study also confirms the excellent material quality of β -Ga₂O₃ since no signature of trap-assisted tunneling is observed, while providing valuable guidance toward the design of high-voltage and low-leakage Schottky barrier diodes.

This work was supported in part by NSF DMREF 1534303 and AFOSR (Nos. FA9550-17-1-0048 and FA9550-18-1-0529) and carried out at the Cornell Nanoscale Facility and CCMR Shared Facilities sponsored by the NSF NNCI program (No. ECCS-1542081), the MRSEC program (No. DMR-1719875), and MRI DMR-1338010.

DATA AVAILABILITY

The data that support the findings of this study are available within this article.

REFERENCES

 ¹M. Higashiwaki and G. H. Jessen, Appl. Phys. Lett. 112, 060401 (2018).
 ²M. Higashiwaki, K. Sasaki, A. Kuramata, T. Masui, and S. Yamakoshi, Appl. Phys. Lett. 100, 013504 (2012).

- ³K. Ghosh and U. Singisetti, J. Appl. Phys. 124, 085707 (2018).
- ⁴Z. Feng, A. F. M. Anhar Uddin Bhuiyan, M. R. Karim, and H. Zhao, Appl. Phys. Lett. 114, 250601 (2019).
- ⁵N. Ma, N. Tanen, A. Verma, Z. Guo, T. Luo, H. Xing, and D. Jena, Appl. Phys. Lett. **109**, 212101 (2016).
 - ⁶M. Higashiwaki, K. Sasaki, H. Murakami, Y. Kumagai, A. Koukitu, A. Kuramata, T. Masui, and S. Yamakoshi, Semicond. Sci. Technol. **31**, 034001 (2016).
 - ⁷A. Kuramata, K. Koshi, S. Watanabe, Y. Yamaoka, T. Masui, and S. Yamakoshi, Jpn. J. Appl. Phys., Part 1 55, 1202A2 (2016).
- ⁸W. Li, Z. Hu, K. Nomoto, R. Jinno, Z. Zhang, T. Q. Tu, K. Sasaki, A. Kuramata, D. Jena, and H. G. Xing, in 2018 IEEE International Electron Devices Meeting (IEDM) (2018), p. 8.5.1.
- ⁹Z. Hu, H. Zhou, Q. Feng, J. Zhang, C. Zhang, K. Dang, Y. Cai, Z. Feng, Y. Gao, X. Kang, and Y. Hao, IEEE Electron Device Lett. **39**, 1564 (2018).
- ¹⁰J. Yang, F. Ren, M. Tadjer, S. J. Pearton, and A. Kuramata, ECS J. Solid State Sci. Technol. 7, Q92 (2018).
- ¹¹Z. Hu, K. Nomoto, W. Li, N. Tanen, K. Sasaki, A. Kuramata, T. Nakamura, D. Jena, and H. G. Xing, <u>IEEE Electron Device Lett</u>. **39**, 869 (2018).
- ¹²W. Li, K. Nomoto, Z. Hu, T. Nakamura, D. Jena, and H. G. Xing, in 2019 IEEE International Electron Devices Meeting (IEDM) (2019), p. 12.4.1.
- ¹³K. Zeng, A. Vaidya, and U. Singisetti, Appl. Phys. Express 12, 081003 (2019).
 ¹⁴K. Tetzner, E. B. Treidel, O. Hilt, A. Popp, S. B. Anooz, G. Wagner, A. Thies, K.
- Ickert, H. Gargouri, and J. Würfl, IEEE Electron Device Lett. **40**, 1503 (2019).
- ¹⁵W. Li, K. Nomoto, Z. Hu, D. Jena, and H. G. Xing, IEEE Electron Device Lett.
 41, 107 (2020).
- ¹⁶A. Kyrtsos, M. Matsubara, and E. Bellotti, Appl. Phys. Lett. **112**, 032108 (2018).
- ¹⁷K. Konishi, K. Goto, H. Murakami, Y. Kumagai, A. Kuramata, S. Yamakoshi, and M. Higashiwaki, Appl. Phys. Lett. **110**, 103506 (2017).
- ¹⁸C. Hou, R. M. Gazoni, R. J. Reeves, and M. W. Allen, IEEE Electron Device Lett. 40, 1587 (2019).
- ¹⁹M. Higashiwaki, K. Konishi, K. Sasaki, K. Goto, K. Nomura, Q. T. Thieu, R. Togashi, H. Murakami, Y. Kumagai, B. Monemar, A. Koukitu, A. Kuramata, and S. Yamakoshi, Appl. Phys. Lett. **108**, 133503 (2016).
- ²⁰W. Li, K. Nomoto, Z. Hu, D. Jena, and H. G. Xing, in 2019 Device Research Conference (DRC) (2019), p. 209.
- ²¹B. Wang, M. Xiao, X. Yan, H. Y. Wong, J. Ma, K. Sasaki, H. Wang, and Y. Zhang, Appl. Phys. Lett. **115**, 263503 (2019).
- ²²L. Zhou, X. Lu, L. Chen, X. Ouyang, B. Liu, J. Xu, and H. Tang, ECS J. Solid State Sci. Technol. 8, Q3054 (2019).
- ²³H. Fu, H. Chen, X. Huang, I. Baranowski, J. Montes, T. H. Yang, and Y. Zhao, IEEE Trans. Electron Devices **65**, 3507 (2018).
- ²⁴T. H. Yang, H. Fu, H. Chen, X. Huang, J. Montes, I. Baranowski, K. Fu, and Y. Zhao, J. Semicond. **40**, 012801 (2019).
- ²⁵E. L. Murphy and R. H. Good, Jr., Phys. Rev. **102**, 1464 (1956).
- ²⁶F. A. Padovani and R. Stratton, Solid State Electron. 9, 695 (1966).
- ²⁷B. Hoeneisen, C. A. Mead, and M.-A. Nicolet, Solid State Electron. 14, 1057 (1971).
- ²⁸H. Peelaers and C. G. Van de Walle, Phys. Status Solidi B **252**, 828 (2015).
- ²⁹Y. Zhang, A. Neal, Z. Xia, C. Joishi, J. M. Johnson, Y. Zheng, S. Bajaj, M. Brenner, D. Dorsey, K. Chabak, G. Jessen, J. Hwang, S. Mou, J. P. Heremans, and S. Rajan, Appl. Phys. Lett. **112**, 173502 (2018).
- ³⁰J. Yang, S. Ahn, F. Ren, R. Khanna, K. Bevlin, D. Geerpuram, S. J. Pearton, and A. Kuramata, Appl. Phys. Lett. **110**, 142101 (2017).
- ³¹L. Zhang, A. Verma, H. G. Xing, and D. Jena, Jpn. J. Appl. Phys., Part 1 56, 030304 (2017).
- ³²W. Li, Z. Hu, K. Nomoto, Z. Zhang, J. Y. Hsu, Q. T. Thieu, K. Sasaki, A. Kuramata, D. Jena, and H. G. Xing, Appl. Phys. Lett. **113**, 202101 (2018).
- ³³H. Fukushima, S. Usami, M. Ogura, Y. Ando, A. Tanaka, M. Deki, M. Kushimoto, S. Nitta, Y. Honda, and H. Amano, Jpn. J. Appl. Phys., Part 1 58, SCCD25 (2019).
- ³⁴W. Mönch, J. Vac. Sci. Technol., B 17, 1867 (1999).
- ³⁵C. Hou, R. M. Gazoni, R. J. Reeves, and M. W. Allen, IEEE Electron Device Lett. 40, 337 (2019).
- ³⁶See http://www.wolfspeed.com/power/products/sic-schottky-diodes/table for "SiC Schottky Diodes" (last accessed February 13, 2020).
- ³⁷P. H. Carey, J. Yang, F. Ren, R. Sharma, M. Law, and S. J. Pearton, ECS J. Solid State Sci. Technol. 8, Q3221 (2019).
- ³⁸T. Harada, S. Ito, and A. Tsukazaki, Sci. Adv. 5, eaax5733 (2019).