

Polarization-induced Zener tunnel diodes in GaN/InGaN/GaN heterojunctions

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By the insertion of thin $\text{In}_x\text{Ga}_{1-x}\text{N}$ layers into Nitrogen-polar GaN p-n junctions, polarization-induced Zener tunnel junctions are studied. The reverse-bias interband Zener tunneling current is found to be weakly temperature dependent, as opposed to the strongly temperature-dependent forward bias current. This indicates tunneling as the primary reverse-bias current transport mechanism. The Indium composition in the InGaN layer is systematically varied to demonstrate the increase in the interband tunneling current. Comparing the experimentally measured tunneling currents to a model helps identify the specific challenges in potentially taking such junctions towards nitride-based polarization-induced tunneling field-effect transistors. © 2015 AIP Publishing LLC.

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Interband tunneling of electrons from filled states in the valence band to empty states in the conduction band occurs at high electric fields in uniformly doped semiconductors. This was Zener's original proposal as the mechanism underlying dielectric breakdown.¹ Esaki soon realized that the built-in electric field in the depletion region of a p-n junction can significantly boost tunneling—he used this principle to demonstrate NDR in Ge p-n junctions, as well as backward diodes.^{2,3}

The interband tunneling current in a reverse-biased p-n homojunction depends sensitively on the doping densities and the energy bandgap. The doping densities N_D and N_A on the n- and p-side control the depletion region thickness $W \sim \sqrt{\frac{2\epsilon_s}{q} \frac{N_A + N_D}{N_A N_D} V_{bi}}$, which is roughly equal to the distance electrons need to tunnel at small negative biases. Here, ϵ_s is the dielectric constant of the semiconductor, q is the electron charge, and $V_{bi} = \frac{kT}{q} \ln \left[\frac{N_A N_D}{n_i^2} \right]$ is the built-in potential of the p-n junction, with n_i the intrinsic carrier concentration generated by thermal interband excitations. The built-in potential is close to the bandgap E_g , and the tunneling barrier height is roughly the energy bandgap.

The above standard p-n homojunction relations hold for shallow donor and acceptor levels. Because of the deep-acceptor level of Mg p-type dopants in GaN, the above relations are modified significantly. Figure 1(a) shows the calculated energy-band diagram, the electric field profile $F(x)$, and mobile carrier concentrations $n(x)$ and $p(x)$ for doping densities $N_D = 5 \times 10^{18}/\text{cm}^3$ and $N_A = 3 \times 10^{19}/\text{cm}^3$ at 300 K. The thermally ionized mobile hole concentration in the p-GaN far from the depletion region is only $p_p \sim 2 \times 10^{17}/\text{cm}^3$ because of a $E_A - E_V = 170$ meV acceptor ionization energy, leading to a built-in voltage $V_{bi} = \frac{kT}{q} \ln \left[\frac{n_n p_p}{n_i^2} \right] \sim 3.25$ V. The mobile carrier densities n_n and p_p far from the junction in the neutral regions of the diode should be used instead of the doping densities N_D and N_A for evaluating the

built-in voltage correctly. The peak electric field reached at the junction is $F_{max} \sim 2.2$ MV/cm, which is high by conventional standards. But because of the combination of a large bandgap, and a long depletion depth (~ 40 nm here), the field is insufficient for appreciable interband tunneling current to flow. Indeed, this is the reason GaN power diodes and transistors can block very high voltages for power electronics applications.

Several recent experimental^{4–7} and theoretical works^{8–11} have discussed that introducing a thin polarization dipole can significantly alter the physics of the p-n junction. Because III-nitride semiconductor heterostructures boast large spontaneous and piezoelectric polarization, one can tunably control the interband tunneling current by suitable design. Figures 1(b) and 1(c) show how introducing thin In(Ga)N polarization dipole layers into a GaN p-n junction alters the energy band diagrams, electric fields, and mobile charge distributions in a fundamental manner.

Introducing a thin In(Ga)N quantum-well layer of thickness t_π introduces a polarization dipole of sheet density σ_π at the heterojunctions, resulting in a (constant) electric field $\mathcal{F}_\pi = q\sigma_\pi/\epsilon_s$ confined in the thin layer, and a potential drop $V_\pi = \mathcal{F}_\pi t_\pi$. As shown in Fig. 1(b), this electric field points in the *same direction* as the built-in junction field of the p-n junction in a N-polar crystal with the p-layer on the right. Since the introduction of an additional layer cannot change the net built-in potential, the area under the electric field curve $\int_{-\infty}^{+\infty} F(x) dx = V_{bi}$ must be unchanged. This means the depletion region in the p- and n-GaN regions must shrink. For example, in Fig. 1(b), a 2 nm $\text{In}_{0.4}\text{Ga}_{0.6}\text{N}$ layer causes a peak electric field to reach ~ 9 MV/cm, which shrinks the depletion region and makes the GaN band edges appear to have a quasi type-II or staggered band-offset. Then, there must exist the critical condition

$$t_\pi \sigma_\pi \geq \frac{\epsilon_s kT}{q^2} \ln \left[\frac{n_n p_p}{n_i^2} \right], \quad (1)$$

for which $V_\pi \geq V_{bi}$, i.e., the polarization-induced voltage drop *exceeds* the built-in junction potential. Then, instead of

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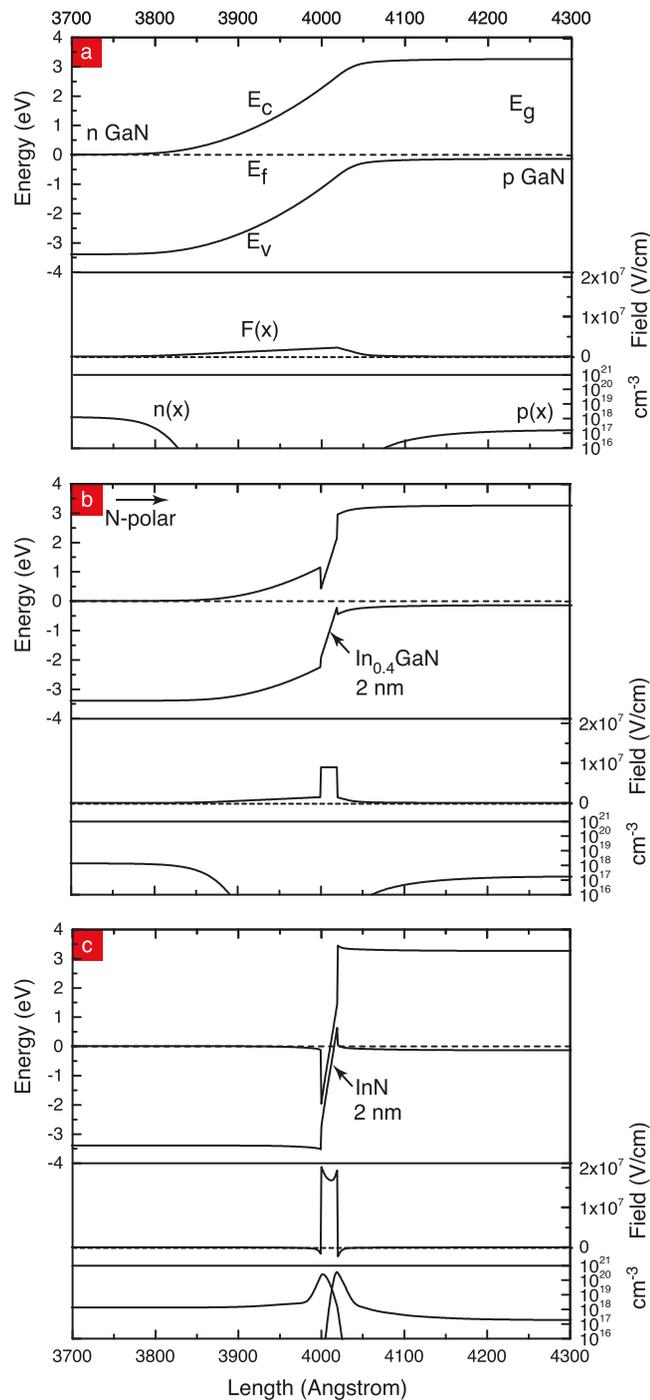


FIG. 1. Calculated energy-band diagrams from self-consistent solutions of the Schrodinger and Poisson equations for (a) a GaN p-n junction, (b) with an InGa_N polarization dipole, and (c) with an InN polarization dipole. The electric field profiles $F(x)$ and the mobile electron and hole distributions $n(x)$ and $p(x)$ are shown. As the Indium content in the InGa_N layer increases, the effective band offset is tuned from Type-II (staggered) in (b) to Type-III (broken) in (c). In going from (b) to (c), the junction is fundamentally changed from that of depletion to that of accumulation.

a depletion region, the junction is forced to become an *accumulation* region. This is indicated in Fig. 1(c), which has a 2 nm InN polarization dipole. The formation of accumulation regions of electrons and holes [Fig. 1(c)] means that the GaN band edges across the junction line up as a Type-III or broken-gap offset. The maximum fields in the polarization dipole are in the ~ 20 MV/cm range, and the field has changed *direction* in the adjacent GaN regions. Such

polarization-induced III-nitride heterojunctions make a traditionally wide-bandgap material system rather counter-intuitively attractive for tunneling field-effect transistors.¹¹ Because of high In composition InGa_N, the bandgap (tunneling barrier) is significantly reduced, the depletion region is non-existent, and the electric field is boosted because of polarization. Polarization charges do not suffer from discrete dopant variations because of their quantum origins in the Berry-phase.^{12–15} The fortunate convergence of these effects can be exploited if thin, strained InGa_N layers of high Indium composition can be grown in GaN p-n junctions. The rest of this work presents an experimental study of the effect of increasing Indium composition on interband tunneling currents, and of temperature dependence of these polarization-induced tunnel junctions.

A series of p-GaN/InGa_N/n-GaN heterostructures with varying Indium compositions were grown by plasma-assisted molecular beam epitaxy (MBE). A ~ 300 nm heavily Si-doped n-GaN layer was grown on commercially available freestanding N-polar n-type GaN substrates with a nominal dislocation density $\sim 5 \times 10^7/\text{cm}^2$ and doping density $\sim 2 \times 10^{19}/\text{cm}^3$ [Fig. 2(b)] at $\sim 700^\circ\text{C}$ under metal rich conditions. The growth rate of this n-doped layer was ~ 130 nm/h. This was followed by a growth interrupt for lowering the substrate temperature and consuming excess Gallium, and then, a $t_\pi \sim 2$ nm thick InGa_N polarization dipole layer was grown at a much lower substrate temperature of $\sim 550^\circ\text{C}$. Based on post-growth structural characterization, the growth rate of the InGa_N dipole layer was ~ 120 nm/h. Finally, a ~ 60 nm Mg-doped p-GaN layer with Mg doping density $N_A \sim 2 \times 10^{19}/\text{cm}^3$, and a 5 nm ohmic cap layer doped at $N_A \sim 7 \times 10^{19}/\text{cm}^3$ was grown after the InGa_N layer, at a growth rate of ~ 125 nm/h as inferred from post-growth TEM analysis. The growth of this p-layer started at 550°C without an interrupt, and the temperature was gradually ramped up after the InGa_N layer was buried. This prevented the InGa_N layer from decomposing, and allowed Mg incorporation with reduced defect density.¹⁶ This transition from the InGa_N layer to a high-quality p-type doped GaN layer remains a challenge in the MBE growth, because raising the temperature for the optimal p-GaN layer causes the decomposition of the InGa_N layer. The surface was capped with a 5 nm thick heavily Mg-doped p-cap layer for contacts. The In compositions were varied over four samples with $x_{In} = 0 \rightarrow 0.15 \rightarrow 0.22 \rightarrow 0.25$, with the first serving as the control p-n junction. Fig. 2(a) shows the X-ray diffraction pattern with cavity resonance fringes due to the existence of an ultra thin InGa_N layer for the $x_{In} = 0.25$ sample; no such fringes are seen in the control sample (not shown). Fig. 2(b) shows the layer structure, and Figs. 2(c) and 2(d) show the transmission electron microscope (TEM) images of the pGa_N/InGa_N/nGa_N heterojunction. The thicknesses and uniformity of the layers are verified from the image. The InGa_N layers are crystalline, and some defect formation was seen in the p-type GaN layers for the high Indium composition junctions.

The samples were then processed into pGa_N/InGa_N/nGa_N tunnel diodes by reactive ion etching (RIE) down to the n-GaN layer, and depositing Ti/Au and Ni/Au metal stacks for n- and p-contacts. Unannealed contacts were formed; the estimated n-type contact resistance is

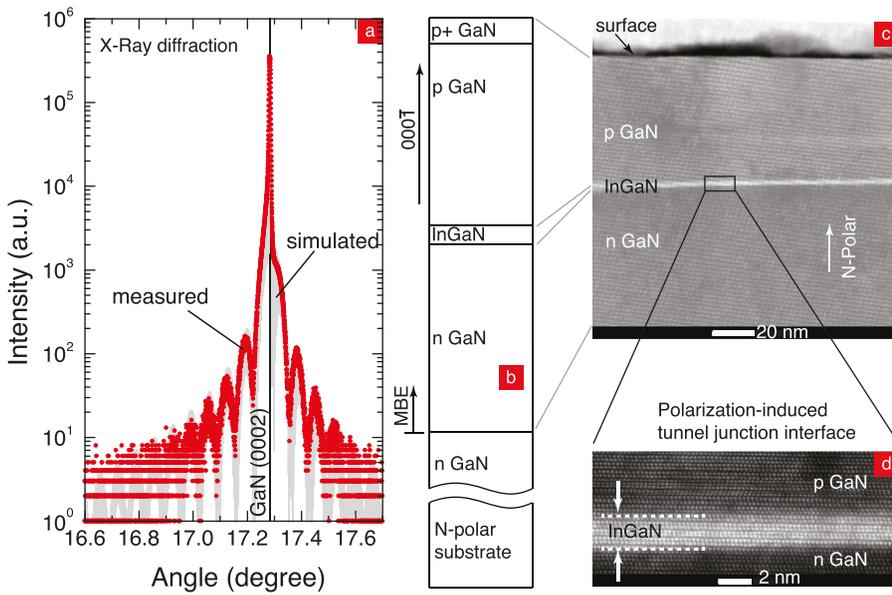


FIG. 2. (a) Interference fringes measured (red dots) and simulated (gray) for a MBE-grown p-GaN/InGaN/n-GaN tunnel heterojunction. The fringes allow estimation of the Indium content. (b) A schematic cross-section of the heterostructure, (c) cross-section TEM image, and (d) high-resolution image showing the incorporation of a $t_{In} \sim 2$ nm thick InGaN polarization dipole.

$\sim 1.4 \times 10^{-6} \Omega \text{cm}^2$, and the p-contact resistance is much higher. The reported electrical characteristics were measured on $30 \times 30 \mu\text{m}$ and $10 \times 10 \mu\text{m}$ diodes. The measured current-voltage (*I-V*) characteristics at 300 K for the $x_{In} = 0.25$ tunnel diode and the control p-n diode are shown in Fig. 3(a). The control p-n diode is rectifying and turns-on at a voltage close to the bandgap of GaN, as expected. It also shows strong near band-edge electroluminescence at forward bias (not shown). The tunnel junction, on the other hand, shows a strong backward-diode behavior, conducting much higher current at reverse bias than forward bias. This is an indication of interband tunneling in such junctions.^{5,6} The current densities reach 10s of kA/cm^2 at high reverse biases.

The tunneling current component is expected to be weakly dependent on temperature to the extent of thermal smearing of the Fermi occupation function. Fig. 3(b) shows

the measured temperature-dependent *I-V* curves of the tunnel junction with $x_{In} = 0.25$ measured from $T = 77 \rightarrow 297$ K, with 20 K increments. The reverse bias current density has a weak temperature dependence, which is indicative of interband tunneling as the transport mechanism. In comparison, the forward bias current density has a much stronger temperature dependence. The ideal forward bias current in a p-n junction is due to the thermionic emission of carriers over the barrier and exhibits a characteristic exponential temperature dependence. However, the forward bias currents measured in Fig. 4(a) are also significantly influenced by defects and trap states. In our structures, these defects are predominantly expected to be in the p-GaN layer because of the low-temperature growth conditions.

The effect of the Indium composition on the reverse-bias tunneling current was examined, as shown in Fig. 4(a). Based on the earlier discussion, the interband tunneling

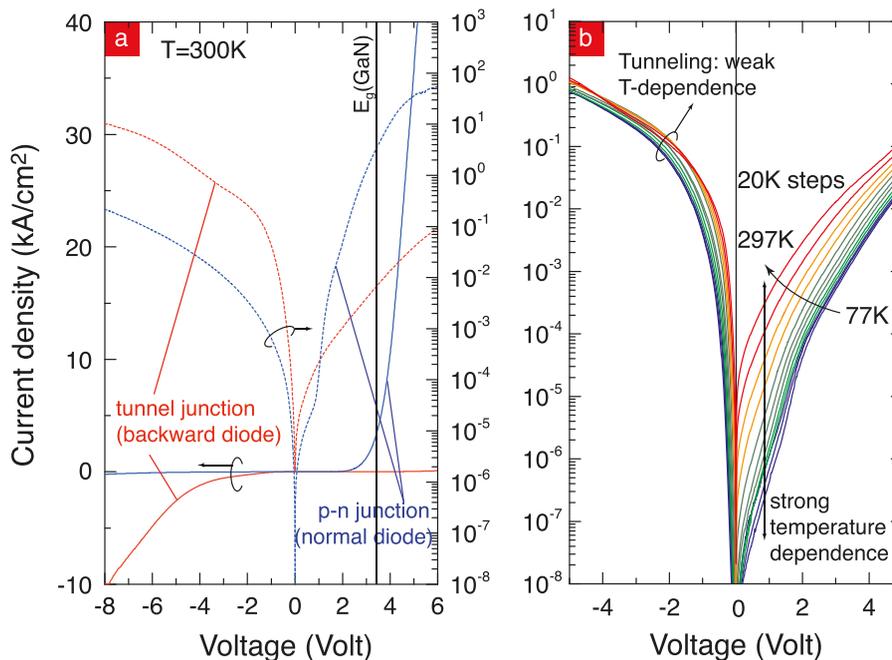


FIG. 3. (a) Measured *I-V* characteristics of a control p-n junction (blue) and the $x_{In} = 0.25$ polarization-induced tunnel-junction (red) in linear (solid) and log (dashed) scales. The tunnel-junction shows backward-diode behavior as a signature of interband tunneling. (b) Temperature dependence of *I-V* curves for the p-GaN/InGaN/n-GaN tunnel junction with $x_{In} = 0.25$. The weak temperature dependence of reverse-bias current supports interband tunneling as the transport mechanism.

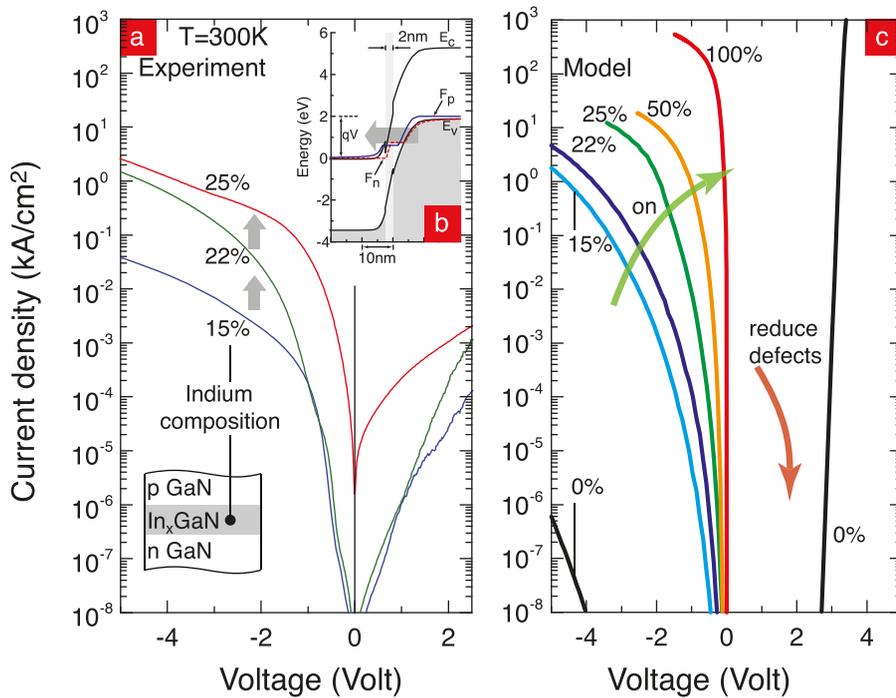


FIG. 4. (a) The effect of increasing polarization charge and smaller bandgaps by increasing the Indium composition x_{In} in the tunnel heterojunction. The reverse bias tunneling current increases with x_{In} . (b) Calculated non-equilibrium energy-band diagram at a bias of -2 V, showing the tunneling window of energies. (c) Calculated reverse-bias currents for various $\text{In}_x\text{Ga}_{1-x}\text{N}$ polarization dipoles, with $0\% \leq x \leq 100\%$. The forward bias current for the control p-n diode is shown and remains of similar orders of magnitude for the Indium compositions for which the calculations converged.

current is expected to increase with increasing x_{In} , which is clearly observed. All three tunnel-junctions operate as backward diodes, conducting more in reverse bias than forward bias. The inset, Fig. 4(b), shows the numerically calculated non-equilibrium energy band diagram for the $x_{In}=0.2$ tunnel junction at a reverse bias voltage of 2 V. The arrows in Fig. 4(a) indicate the increase of current density with Indium composition.

Fig. 4(c) shows the *calculated* current density for various polarization induced tunnel junctions with compositions ranging from $0\% \rightarrow 15\% \rightarrow 22\% \rightarrow 25\% \rightarrow 50\% \rightarrow 100\%$ Indium in the $\text{In}_x\text{Ga}_{1-x}\text{N}$ layer. The tunnel diodes have been simulated with a commercial drift-diffusion based TCAD package (Synopsys Sentaurus).¹⁷ In the simulation, the interband tunneling is treated by a WKB (Wentzel-Kramers-Brillouin)-based nonlocal band-to-band generation/recombination model. Both the reverse-bias tunneling current and the normal forward bias currents are calculated; the forward bias characteristics of the control p-n homojunction are shown. As can be seen, the increase in the tunneling current with the Indium composition x_{In} is in qualitative agreement with the numerical simulations of current densities. It is clear that significantly higher x_{In} than obtained here is necessary to achieve $\sim 10^6$ A/cm² currents at small voltages.

Since tunneling currents are very sensitive to thicknesses and compositions, a large number of diodes were measured and analyzed to strengthen the claims against artifacts due to defects and variations. A brief discussion illustrates these issues. Among the three experimental I-V curves in Fig. 4(a), it is seen that the forward bias current density of the 25% InGaN sample is higher than the diodes for 22% and 15% compositions, indicating the leakage is higher in this diode. As a result, the reverse-bias current density is also higher for this tunnel diode. One may obtain a crude estimate of the “actual” tunneling current by subtracting out forward bias leakage by reflecting around the origin. This will make the low-bias reverse currents close

for all three diodes, similar to the calculated reverse currents in Fig. 4(b). But the high-bias reverse currents say, beyond -1.0 V will not change significantly from Fig. 4(a). Comparing the TCAD model calculation in Fig. 4(b), a significant boost in tunneling ($\sim 100\times$) is expected going from 15 and 22%, to 25%, around say, -2.0 V reverse bias. This expected feature is clearly observed in the experimental data in Fig. 4(a). For low reverse biases, the expected differences are buried in the leakage, and at high reverse biases, the series resistance of contacts and p-GaN restrict the measured current. The boost in tunneling indicated by arrows at -2.0 V in Fig. 4(a) lies within a window where the experiment may be safely compared with the theory, and the two are consistent.

The reduction of defects by improved growth conditions, and the incorporation of increasing Indium compositions in the InGaN layer are the major challenges moving forward. This may require going from planar junctions to nanowire or other geometries that allow the incorporation of significant strain due to high x_{In} . But the benefits of achieving such structures go beyond the TFETs and associated devices. Successful realization of such heterostructures will help probe the electronic bandstructure, polarization fields, and band offsets. These heterostructures can potentially host exotic electronic states: for example, the strong mixing of the electron and hole states across the InN layer in Fig. 1(c), combined with the very high electric field-induced Rashba effect causes an effective band-inversion—as highlighted by the broken gap alignment. In these structures, topological edge-states are predicted to occur.¹⁸ The search for such exotic electronic states of matter offer strong motivation to address the significant challenges in realizing these heterostructures in the future.

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- ¹C. Zener, *Proc. R. Soc. A* **145**, 523 (1934).
- ²L. Esaki, *Phys. Rev.* **109**, 603 (1958).
- ³S. M. Sze and K. K. Ng, *Physics of Semiconductor Devices* (Wiley, 2006).
- ⁴M. J. Grundmann and U. K. Mishra, *Phys. Status Solidi C* **4**, 2830 (2007).
- ⁵J. Simon, Z. Zhang, K. Goodman, H. Xing, T. Kosel, P. Fay, and D. Jena, *Phys. Rev. Lett.* **103**, 026801 (2009).
- ⁶S. Krishnamoorthy, D. N. Nath, F. Akyol, P. S. Park, M. Esposto, and S. Rajan, *Appl. Phys. Lett.* **97**, 203502 (2010).
- ⁷M. Kaga, T. Morita, Y. Kuwano, K. Yamashita, K. Yagi, M. Iwaya, T. Takeuchi, S. Kamiyama, and I. Akasaki, *Jpn. J. Appl. Phys., Part 1* **52**, 08JH06 (2013).
- ⁸M. F. Schubert, *Phys. Rev. B* **81**, 035303 (2010).
- ⁹D. Jena, J. Simon, A. Wang, Y. Cao, K. Goodman, J. Verma, S. Ganguly, G. Li, K. Karda, V. Protasenko, C. Lian, T. Kosel, P. Fay, and H. Xing, *Phys. Status Solidi A* **208**, 1511 (2011).
- ¹⁰M.-C. Tsai, B. Leung, T.-C. Hsu, and Y.-K. Kuo, *IEEE J. Lightwave Technol.* **31**, 3575 (2013).
- ¹¹W. Li, S. Sharmin, H. Ilatikhameneh, R. Rahman, Y. Lu, J. Wang, X. Yan, A. Seabaugh, G. Klimeck, D. Jena, and P. Fay, *IEEE Explor. Solid-State Comput. Devices Circuits* **1**, 28 (2015).
- ¹²R. D. King-Smith and D. Vanderbilt, *Phys. Rev. B* **47**, 1651 (1993).
- ¹³R. Resta, *Rev. Mod. Phys.* **66**, 899 (1994).
- ¹⁴F. Bernardini, V. Fiorentini, and D. Vanderbilt, *Phys. Rev. B* **56**, R10024 (1997).
- ¹⁵D. Xiao, M. Chang, and Q. Niu, *Rev. Mod. Phys.* **82**, 1959 (2010).
- ¹⁶J. Simon and D. Jena, *Phys. Status Solidi A* **205**, 1074 (2008).
- ¹⁷*Sentaurus Device User Guide*, Version I—2013.12.
- ¹⁸M. S. Miao, Q. Yan, C. G. Van de Walle, W. K. Lou, L. L. Li, and K. Chang, *Phys. Rev. Lett.* **109**, 186803 (2012).